Workshop Program

ACM SIGPLAN 1998 Workshop on Languages, Compilers, and Tools for Embedded Systems (LCTES'98)

Montreal, Canada, June 19-20, 1998
(in conjunction with PLDI'98)

http://www.informatik.hu-berlin.de/~mueller/lctes98
(On-line proceedings for a limited time: August — September 1998)

Chairs: Frank Mueller (mueller@informatik.hu-berlin.de), Azer Bestavros (best@bu.edu)

Workshop Schedule, Friday June 19, 1998

12:30-13:15 - Invited Talk: Applying UML to Complex Real-Time Systems, Bran Selic (ObjecTime, USA)
13:30-15:30 - Session 1

- **Integrating Path and Timing Analysis using Instruction-Level Simulation Techniques** (30 minutes) Thomas Lundqvist, Per Stenström (Chalmers University of Technology, Sweden)
- **On Predicting Data Cache Behavior for Real-Time Systems** (30 minutes) Christian Ferdinand, Reinhard Wilhelm (Universität des Saarlandes, Germany)
- **Automatic Accurate Time-Bound Analysis for High-Level Languages** (20 minutes) Yanhong A. Liu, Gustavo Gomez (Indiana University, USA)
- **Extending RT-Linux to Support Flexible Hard Real-Time Systems with Optional Components** (20 minutes) A. Terrasa, A. Espinosa, A. Garcia-Fornes (Universidad Politecnica de Valencia, Spain)
- **Limited Preemptible Scheduling to Embrace Cache Memory in Real-Time Systems** (20 minutes) Sheayun Lee, Chang-Gun Lee, Minsuk Lee, Sang Lyul Min, Chong Sang Kim (Seoul National University, Korea)

15:30-16:00 - Break
16:00-18:00 - Session 2

- **A Uniform Reliable Multicast Protocol with Guaranteed Response Times** (30 minutes) Laurent George, Pascale Minet (INRIA, France)
- **A Tool to Assist in Fine-Tuning and Debugging Embedded Real-Time Systems** (30 minutes) Gaurav Arora, David B. Stewart (University of Maryland, USA)
- **Debugging Distributed Implementations of Modal Process Systems.** (20 minutes) Ken Hines, Gaetano Borriello (University of Washington, USA)
- **Using Inferno to Run Java on small devices** (20 minutes) C. F. Yurkoski, L. R. Rau, B. K. Ellis (Bell Labs, USA)
- **TurboJ, a Java Bytecode-to-Native Compiler** (20 minutes) Michael Weiss, Francois de Ferriere, Bertrand Delsart, Christian Fabre, Frederick Hirsch, E. Andrew Johnson, Vania Joloboff, Fridtjof Siebert, Xavier Spengler (Open Group Research Institute, USA)

19:00-??:?? - Reception and Dinner

Saturday, June 20, 1998

7:00-8:00 - Breakfast
8:00-8:45 - Invited Talk: Evaluating ASIC, DSP, and RISC Architectures for Embedded Applications, Marc Campbell, (Northrop Grumman, USA)
8:45-9:00 - Break
9:00-10:30 - Session 3

- **Cache-Sensitive Pre-Runtime Scheduling** (30 minutes) Daniel Kästner, Stephan Thesing (Universität des Saarlandes Germany)
- **Priority Assignment for Embedded Reactive Real-Time Systems** (20 minutes) Felice Balarin (Cadence Berkeley Labs, USA)
- **Mapping an Embedded Hard Real-Time Systems SDL Specification to an Analyzable Task Network - A Case Study** (20 minutes) Thomas Kolloch, Georg Färber (TU München, Germany)
- **Efficient User-level I/O in the ARX Real-Time Operating System** (20 minutes) Yangmin Seo, Jungkeun Park, Seongsoo Hong (Seoul National University, Korea)

10:30-11:00 - Break
11:00-13:00 - Session 4

- **Machine Descriptions to Build Tools for Embedded Systems** (30 minutes) Norman Ramsey, Jack W. Davidson (University of Virginia, USA)
- **Non-Local Instruction Scheduling with Limited Code Growth** (30 minutes) Keith D. Cooper, Philip J. Schielke (Rice University, USA)
- **An Efficient Data Partitioning Method for Limited Memory Embedded Systems** (20 minutes) Anantharaman Sundaram, Santosh Pande (University of Cincinnati, USA)
- **A Design Environment for Counterflow Pipeline Synthesis** (20 minutes) Bruce R. Childers, Jack W. Davidson (University of Virginia, USA)
- **End-to-end Optimization in Heterogeneous Distributed Real-Time Systems** (20 minutes) Seonho Choi (Bowie State University, USA)

Registration Information

Please print or type all information to avoid errors on your badge.

To qualify for the lower rates, registration forms with full payment must be postmarked by May 15, 1998.

- Please make Checks and Money Orders payable in U.S. Dollars to **ACM SIGPLAN '98**.
- Phone, Faxed, and on-line registrations must be paid by VISA, MasterCard, or American Express.
- Government vouchers and purchase orders are not accepted.

Refund requests for cancellations are subject to a $50 administrative fee and must be postmarked by May 15, 1998. After this date, cancellations and no-shows are liable for the full fees.

PLDI '98 registration includes three continental breakfasts, two lunches, an evening reception and panel session on Wednesday evening, a reception and awards dinner on Thursday, coffee breaks, and conference proceedings.

Student registration includes conference proceedings and Wednesday evening reception and panel session, but does not include breakfasts, lunches or the Thursday evening reception and awards dinner.

Additional tickets for lunch at $16 per day, and the Thursday reception at $28 may be purchased separately on-site.

Tutorial registration includes a continental breakfast, lunch, and a copy of the tutorial notes.

PASTE '98 Workshop includes a continental breakfast, lunch, an evening reception, and workshop proceedings.

LCTES '98 Workshop includes a dinner Friday evening, continental breakfast, and workshop proceedings.

For more information, please visit the home page for PLDI '98:

http://www.cs.virginia.edu/pldi98

Late Fees Begin May 15, '98

Send Registration form and FULL payment to:

ACM SIGPLAN '98
c/o Registration Systems Lab
2060 Goldwater Court
Maitland, FL 32751

or Fax to: +1 407 628 3186

or register on-line at:
http://www.reg-master.com/pldi98.html

For registration inquiries:
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mann@reg-master.com

PLDI '98 Advance Registration

Name ____________________

Affiliation ____________________

Address ____________________

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Phone ____________ Fax ____________

E-mail ____________________

ACM Membership # ____________________

☐ I do not want my name on a mailing list sent electronically to attendees only.

Dietary Requirements: ☐ Vegetarian ☐ Vegan ☐ Kosher

Special needs (specify): ____________________

Fee Schedule (in US$)

On or Before May 15, 1998 (please circle **all** applicable fees)

	ACM & SIGPLAN	ACM or SIGPLAN	Non-member	Student
PLDI '98	310	335	360	150
Tutorial (Half Day)	110	125	135	50
PASTE '98 Workshop	95	95	105	95
LCTES '98 Workshop	90	90	100	90

On-site and After May 15, 1998 (please circle **all** applicable fees)

	ACM & SIGPLAN	ACM or SIGPLAN	Non-member	Student
PLDI '98	360	385	410	175
Tutorial (Half Day)	120	135	145	60
PASTE '98 Workshop	95	95	105	95
LCTES '98 Workshop	90	90	100	90

Payment Computation

PLDI '98 conference fee .... $ ________

Tutorial (Half Day morning): ☐ I ☐ II ..... $ ________

Tutorial (Half Day afternoon): .... $ ________

PASTE '98 workshop .... $ ________

LCTES '98 workshop .... $ ________

Total payment: $ ____________

Forms without payment will not be accepted. Please make Checks and Money Orders payable to <u>ACM SIGPLAN '98</u>. If paying by VISA, MasterCard, or American Express, please complete the following:

Card # ____________________

Expires ____________ Signature ____________________

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Billing address if different from above